

**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2188**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No.: 10/813,857	§	Examiner:	Eland, Shawn
Filed: March 31, 2004	§	Group/Art Unit:	2188
Inventor(s):	§	Atty. Dkt. No:	5681-13501
Robert E. Cypher	§	Conf. No.	5571
	§		
	§		
	§		
Title: Multi-Node System with	§		
Response Information in	§		
Memory	§		
	§		
	§		
	§		
	§		

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**RESPONSE TO FINAL OFFICE ACTION OF  
July 9, 2007**

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 9, 2007, to further highlight why the application is in condition for allowance.